FIG 1

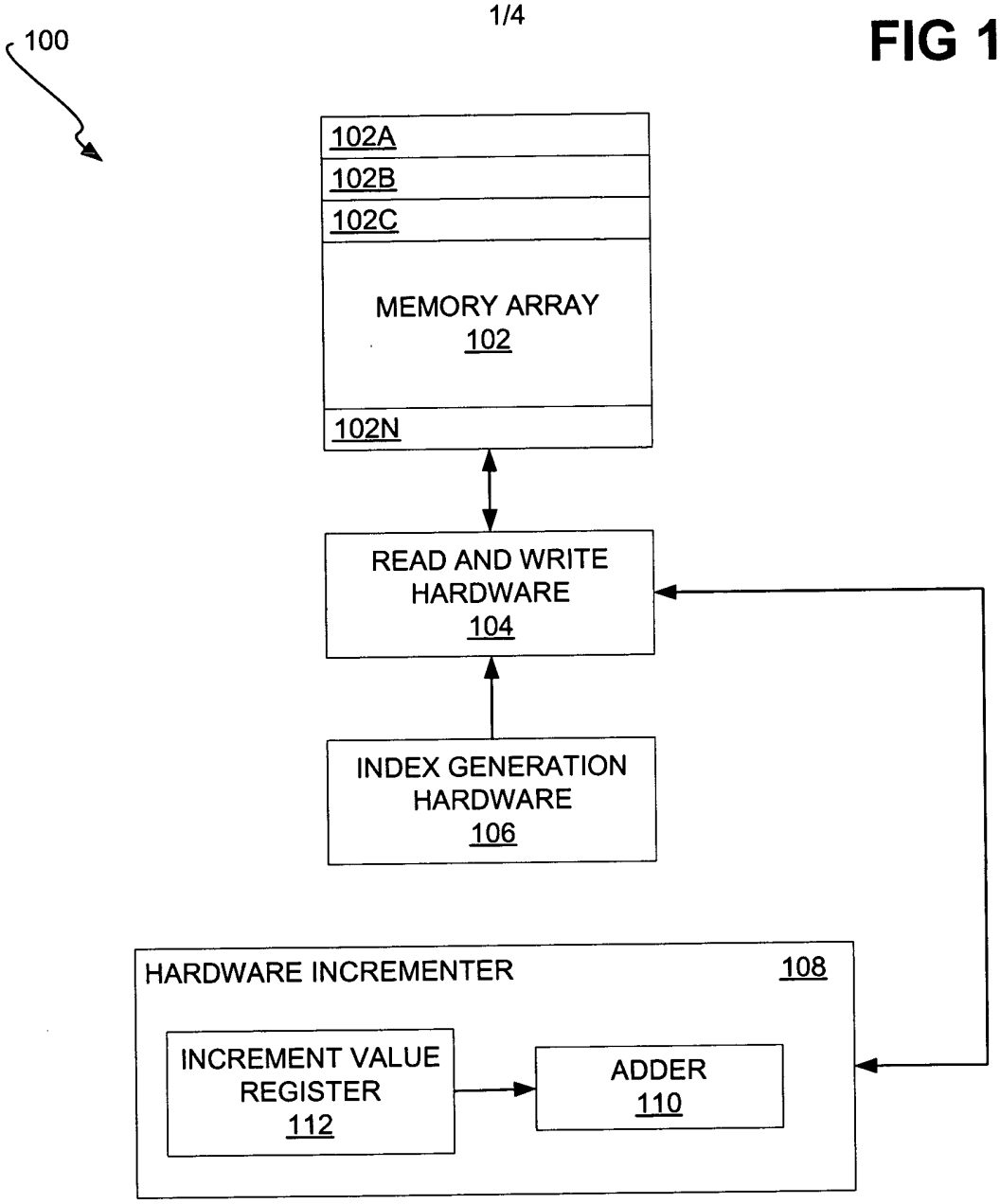

100
102A
102B
102C
MEMORY ARRAY
102
102N
READ AND WRITE
HARDWARE
104
INDEX GENERATION
HARDWARE
106
HARDWARE INCREMENTER
108
INCREMENT VALUE
REGISTER
112
ADDER
110

FIG 2

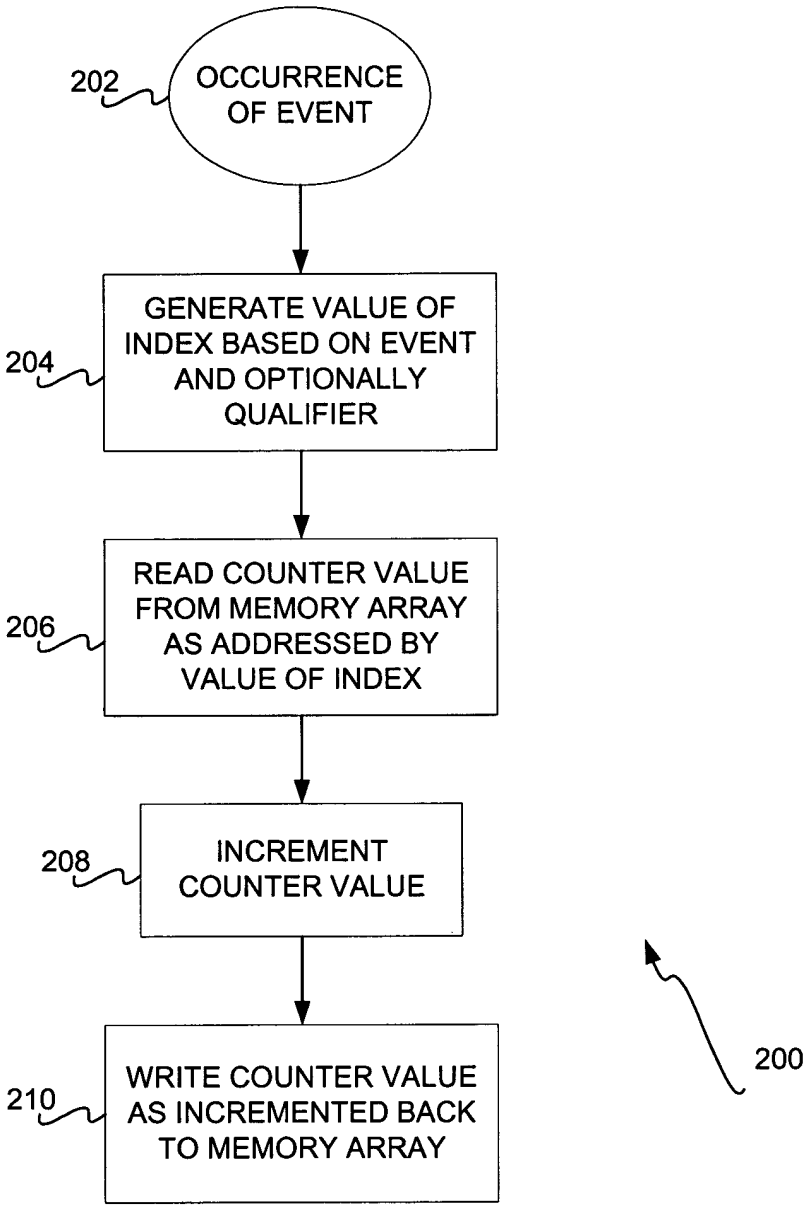

202
OCCURRENCE OF EVENT
204
GENERATE VALUE OF INDEX BASED ON EVENT AND OPTIONALLY QUALIFIER
206
READ COUNTER VALUE FROM MEMORY ARRAY AS ADDRESSED BY VALUE OF INDEX
208
INCREMENT COUNTER VALUE
210
WRITE COUNTER VALUE AS INCREMENTED BACK TO MEMORY ARRAY
200

FIG 3

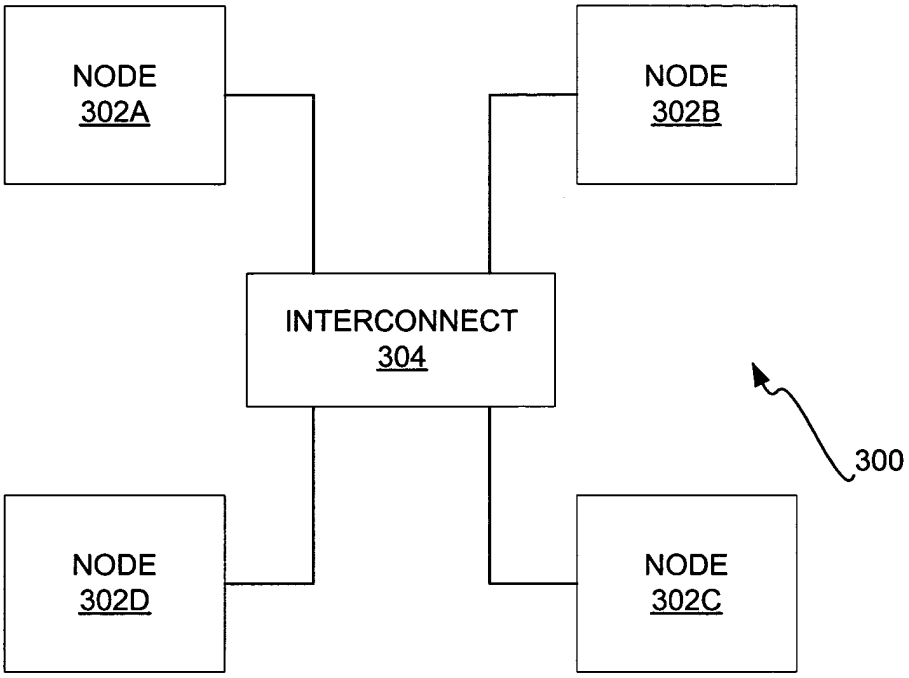

NODE
302A
NODE
302B
INTERCONNECT
304
300
NODE
302D
NODE
302C

FIG 4

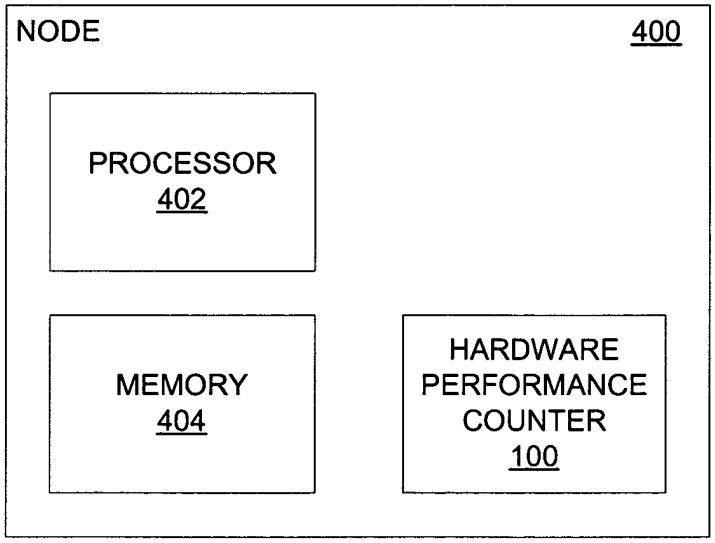
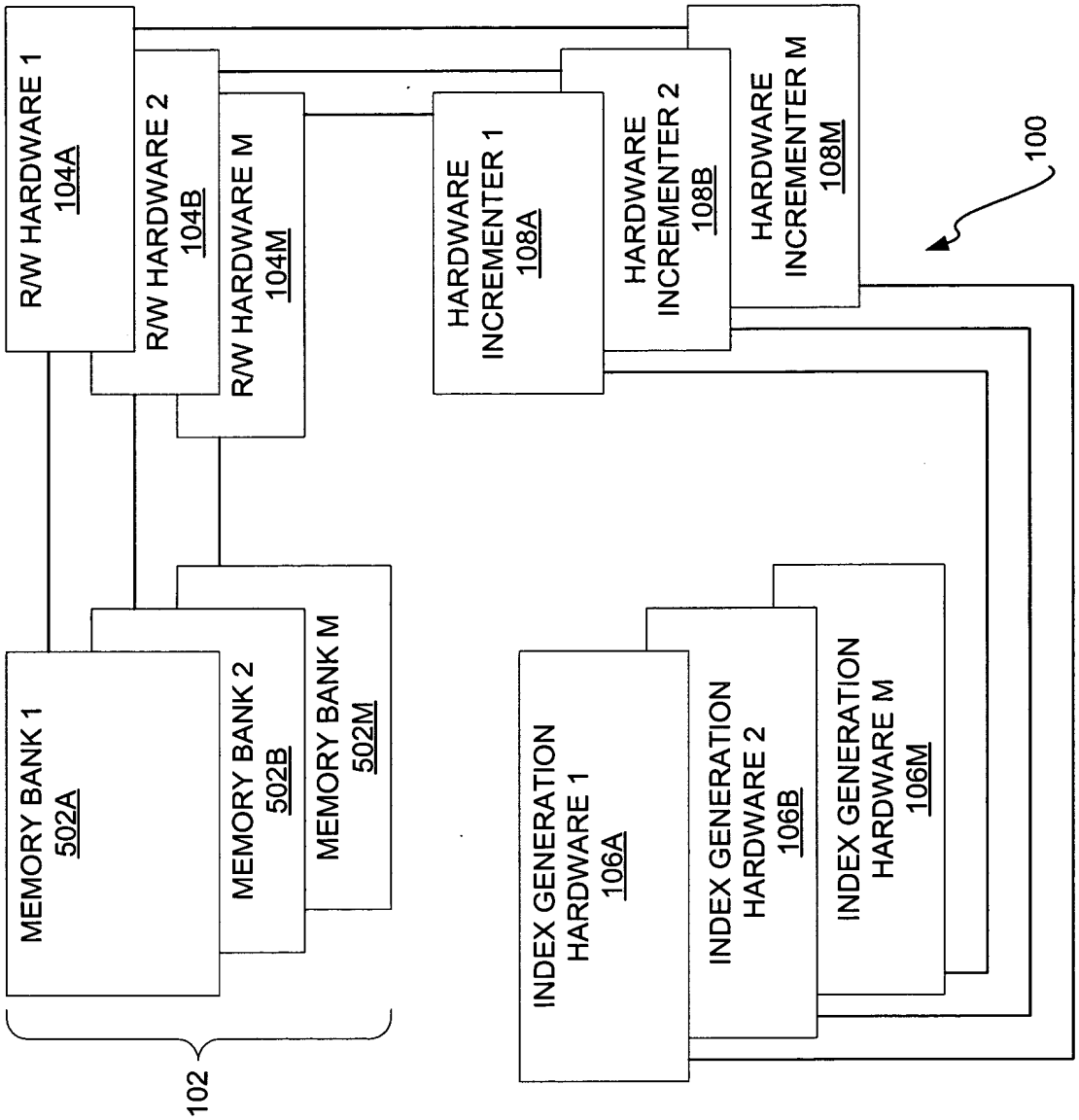

NODE
400
PROCESSOR
402
MEMORY
404
HARDWARE
PERFORMANCE
COUNTER
100

FIG 5


R/W HARDWARE 1
104A
R/W HARDWARE 2
104B
R/W HARDWARE M
104M
HARDWARE INCREMENTER 1
108A
HARDWARE INCREMENTER 2
108B
HARDWARE INCREMENTER M
108M
100
MEMORY BANK 1
502A
MEMORY BANK 2
502B
MEMORY BANK M
502M
102
INDEX GENERATION HARDWARE 1
106A
INDEX GENERATION HARDWARE 2
106B
INDEX GENERATION HARDWARE M
106M